

FORM PTO-1449	ATTY. DOCKET NO. 0081-012	SERIAL NO. 09/051,263
SEP 20 2007	APPLICANT Shaw (et al)	
PATENT & TRADE MARK OFFICE	FILING DATE August 7, 1998	GROUP 2183

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
AA	3,968,501	July 6, 1976	Gilbert	364	7
AB	4,075,691	February 21, 1978	Davis et al.	364	200
AC	4,144,562	March 13, 1979	Cooper	364	200
AD	4,224,676	September 23, 1980	Appelt	364	712
AE	4,236,152	November 25, 1980	Masuzawa et al.	340	747
AF	4,295,193	October 13, 1981	Pomerene	364	200
AG	4,328,557	May 4, 1984	Gastinel	364	900
AH	4,334,268	June 8, 1982	Boney et al.	364	200
AI	4,361,869	November 30, 1982	Johnson et al.	364	200
AJ	4,449,201	May 15, 1984	Clark	364	900
AK	4,541,111	September 10, 1985	Takashima et al.	381	51
AL	4,566,063	January 21, 1986	Zolnowsky, et al.	364	200

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION
						YES NO
CA	JP SHO 61(1986)-127228	June 14, 1984	Hitachi Ltd.	H03L	7/16	
CB	EP 85105578.0	November 18, 1984	INMOS Limited	G06F	9/46	

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

DA	"Engineering Data IMS T414M Transputer"; August 1987, pp. 1-39.
DB	"TLES-42,47,470 User's Manual"; April 1986, pp. I. MCU47-117 through 203.
DC	"SM550/SM551/SM552 4-bit Microcomputer (Controller)"; 1982-1983, pp. 41-108.
DD	"Microprocessors in Brief"; Robert C. Stanley, IBM J. Res. Develop., Vol. 29, No. 2, March 1985, pp. 115-116.
DE	"Microprocessor System Bus"; IEEE Std 796-1983, December 1983, pp. 9-46.
DF	"Introduction to VLSI Systems"; Mead & Conway, 1980, pp. 1-429.
DG	MOSTEK CORP., "Mostek 1981 3870/F8 Microcomputer Data Book", February 1981, pp. III-76 through III-77, III-100 through III-129, and VI-1 through VI-11.

EXAMINER	DATE CONSIDERED
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	AM	4,630,195	December 16, 1986	Hester et al.	364	200
	AN	4,660,155	April 21, 1987	Thaden et al.	364	521
	AO	4,689,581	August 25, 1987	Talbot	331	1
	AP	4,691,124	September 1, 1987	Ledzius	307	303
	AQ	4,718,648	December 1, 1987	Hanamura et al.	307	443
	AR	4,714,994	December 22, 1987	Oklobdzija et al.	712	207
	AS	4,718,081	January 5, 1988	Brenig	379	60
	AT	4,750,111	June 7, 1988	Crosby, Jr., et al.	364	200
	AU	4,763,297	August 9, 1988	Zahlenhoff	364	900
	AV	4,766,567	August 23, 1988	Kato	364	900
	AW	4,803,621	February 7, 1989	Kelly	364	200
	AX	4,805,091	February 14, 1989	Thiel et al.	364	200

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	DH	MOSTEK CORP., Advertisement, <i>EDN</i> , November 20, 1976.
	DI	UNITED TECHNICAL PUBLICATIONS, IC Master, 1980, pp. 1 and 2016-2040.
	DJ	GUTTAG, K. M., "The TMS34010: An Embedded Microprocessor", <i>IEEE Micro</i> , vol. 8, no. 3, May 1988, pp. 39-52.
	DK	HITACHI AMERICA LTD., "8-Bit Single-Chip Microcomputer Data Book", July 1985, Table of Contents and pp. 251-279.
	DL	"TPL Web Site, <a href="http://www.tplgroup.net/patents/index.php">www.tplgroup.net/patents/index.php</a> ", August 3, 2006, 3 pgs.
	DM	"The IBM System/ 360 Model 91: Machine Philosophy and Instruction-Handling", D.W. Anderson, <i>IBM Journal</i> , January 1967, pp. 11-20.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	AY	4,890,225	December 26, 1989	Ellis, Jr. et al.	712	234
	AZ	4,914,578	April 3, 1990	MacGregor et al.	364	200
	BA	4,931,748	June 5, 1990	McDermott	331	1
	BB	4,933,835	June 12, 1990	Sachs et al.	364	200
	BC	4,940,553	July 17, 1990	Dalrymple et al.	364	900
	BD	4,979,102	December 18, 1990	Tokuume	364	200
	BE	4,989,133	January 29, 1991	May et al.	364	200
	BF	5,021,991	June 4, 1991	MacGregor et al.	364	900
	BG	5,081,574	January 14, 1992	Larson et al.	395	375
	BH	5,097,437	March 17, 1992	Larson	395	775
	BI	5,103,499	April 7, 1992	Miner et al.	395	162
	BJ	5,134,701	July 28, 1992	Mueller et al.	395	500

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
						YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	DN	"GE 625/635 Programming Reference Manual", <i>General Electric</i> , July 1964, pp. I-1, II-3, II-127-128, III-62.
	DO	"Clipper 32-bit Microprocessor: Introduction to the Clipper Architecture", March 1986, pp. 2-1 through 2-8.
	DP	"IBM RT PC Romp", Simpson & Hester, <i>IBM Systems Journal</i> , Vol. 26, No. 4, 1987, pp. 60, 347-56.
	DQ	"i860 64-bit Microprocessor: Advance Information", February 1989, pp. 7-27.
	DR	"Intel's 80960: An Architecture Optimized for Embedded Control", David P. Ryan, <i>IEEE Micro</i> , June 1988, pp. 64-72.
	DS	"IBM RT Personal Computer Technology", <i>IBM Corp.</i> , 1986, p. 60.

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	BR	5,146,592	September 8, 1992	Pfeiffer et al.	395	157
	BL	5,148,385	September 15, 1992	Frazier	708	426
	BM	5,226,147	July 6, 1993	Fujishima et al	395	425
	BN	5,237,699	August 17, 1993	Little	395	750
	BO	5,326,513	June 28, 1994	Tanaka	395	500
	BP	5,353,497	October 4, 1994	Fujishima et al.	395	425
	BQ	5,379,438	January 3, 1995	Bell et al.	395	800
	BR	5,421,000	May 30, 1995	Fortino et al.	395	425
	BS	5,459,846	October 17, 1995	Hyatts	395	421.04
	BT	5,530,890	June 25, 1996	Moore et al.	395	800
	BU	5,604,915	February 18, 1997	Moore et al.	395	800
	BV	5,659,703	August 19, 1997	Moore et al.	395	436
	BW	5,784,584	July 21, 1998	Moore et al.	395	376
	BX	5,809,336	September 15, 1998	Moore et al.	395	845
	BY	6,598,148	July 22, 2003	Moore et al.	712	32

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
						YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	DT	"MIPS: A Microprocessor Architecture", John Hennessy et al., <i>IEEE</i> , 1982, pp. III - 62.
	DU	"Multi-Microprocessor Systems", Y. Parker, <i>APIC Studies in Data Processing</i> , no. 18, Chapter. 1, pp.1-23 (1983).
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